

### Hardware | Framegrabber | Vision Boards

### **SILICONSOFTWARE**

V-Series GigabitEthernet



# microEnable IV VQ4-GE

**Camera Interface** 

**GigE** Vision

Power over Camera Link

Programmable quad port PCIe frame grabber for GigE Vision

GiG

The microEnable IV VQ4-GE is a quad-port frame grabber for four independent GigE vision cameras.

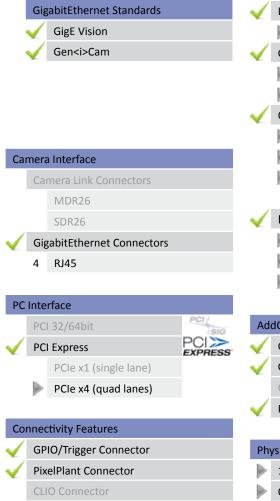
All integrated image acquisition and image pre-processing functions of the microEnable IV VQ4-GE are executed on the system FPGA in real time, and offer high performance and robust and reliable acquisition technology at the same time.

Additionally you can use SmartApplets for loading application-related image processing operations. The processing takes place with high algorithmic quality and relieves the softwarerelated image processing.

For implementing customized image processing the vision FPGA is able to be programmed with the graphical tool VisualApplets. The microEnable IV VQ4-GE is pre-licensed for VisualApplets (Base) and SmartApplets (Base).

With its wide range of functions and its high performance, the microEnable IV VQ4-GE enables the professional use of GigabitEthernet in the Machine Vision industry.

With the help of image reconstruction the system load is dramatically decreased. Digital interfaces for signal input and output allow for a control of external devices with low latencies, and a Software Development Kit (SDK) tailored for Machine Vision enables the comfortable integration of application of your own.



Aco	quisition Features
	Camera Pixel Clock Support
	85 MHz
$\checkmark$	Area Scan Cameras
	8k * 4k max. image size
$\checkmark$	Line Scan Cameras
	16k max. image width
$\checkmark$	Grayscale Cameras
	8bit resolution
	16bit resolution
$\checkmark$	Color Cameras
	24bit resolution (RGB)
	48bit resolution (RGB)
	24bit resolution (Bayer CFA)
	36bit resolution (Bayer CFA)
$\checkmark$	Mixed Mode (requires VisualApplets)
	Area Scan + Line Scan Cameras
	Grayscale + Color Cameras
	Arbitrary Combinations
Ad	dOn Products
$\checkmark$	GPIO Boards
$\checkmark$	GPIO Boards, opto-isolated
	CLIO (Camera Link Replicator)
$\checkmark$	PixelPlant (Processing Extension)
Phy	vsical Board Properties
	168 mm length x 111 mm height
	Operating temperature 0°C - 40°C

Any information without obligation. Technical specifications and scope of delivery are liability-free and valid until revocation. Mistakes are excepted

SILICONSOFTWARE Steubenstrasse 46

Product Features

Acquisition Buffer

512 MB DDR-RAM

**FPGA System Processor** 

**FPGA** Vision Processor Xilinx Spartan 2

Xilinx Spartan 3

760 MBytes/sec.

Sustainable Transfer Rate (max.)

D-68163 Mannheim Fax: Germany

Phone: +49(0)621 789507-0 +49(0)621 789507-10

Email: info@silicon-software.de

Web: www.silicon-software.de www. silicon-software.com





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#### Image Acquisition Features

- Synchronous Acquisition Process 0
- 6 Multi-Camera Acquisition Ability
- 0 Knee Lookup Table
- Basic Image Processing, e. g. Bright-6 ness, Contrast and Gamma Correction
- 0 **Real-Time Bayer Conversion**
- 0 Internal 16bit Processing
- 0 Regions of Interest (ROI)
- 0 Hardware Generated Image Number
- 0 Reads Gen<i>Cam Configuration
- 0 Highly Customizable I/O System
- 8 Dig I/O Signals
  - ... and further ones

### Special Features

- Support of Jumbo Packets 6
- Automatic Image Reconstruction from 6 Data Packets
- Reduction of Interupt Load to 1 IRQ/img -
- Reduction of CPU Load by Optimized Drivers 0

... and further ones

#### Software Products

- **Device Drivers**
- **Firmware Flasher**
- microDisplay
- microDiagnostics
- **GigE Explorer**
- microEnable SDK

Processing Libraries							
incl.		AcquisitionApplets					
incl.		SmartApplets Base					
	opt.	SmartApplets Extended					
incl.		VisualApplets Base					
	opt.	VisualApplets Blob					
	opt.	VisualApplets Compression					





SmartApplets enabled Carolina April FTE VisualApplets enabled

Ор	eration Systems	
$\checkmark$	Windows XP	32bit
$\checkmark$	Windows Vista	32bit
$\checkmark$	Windows 7	32bit
	Linux (Kernel 2 6 23+)	32hit

Hardware/Software Compatibility

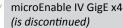


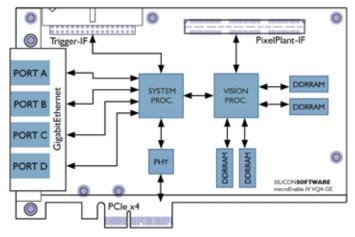
64bit

64bit

64bit

64bit





Scematic layout of microEnable IV VQ4-GE

Supported Features Sorted by Hardware Applets for microEnable IV VQ4-GE				Quad Area Gray 16	Quad Area RGB 24	Quad Line Gray 8	Quad Line Gray 16	Quad Line RGB 24
Camera Support	GigE Vision							
	Gen <i>Cam</i>	•	•	•		•	•	
Camera Type	Area Scan / Line Scan	А	А	А	А	L	L	L
	GrayScale / RGB / Bayer	BAY	G	G	RGB	G	G	RGB
	Supported Cameras	4	4	4	4	4	4	4
Color Processing	White Balancing							
	Bayer Bilinear Algorithm	•						
	Look-up Table							
	Image Enhancements							
Image Enhancement	Median Filter							•
	Image Processing							
Image Correction	Image Format Reconstruction	•	•	•		•	•	•
Signal Control	Software trigger	•						
	Digital input signals	2	2	2				
	Digital output signals	2	2	2				
	I/O boards opto/TTL available	•	•	•	•	•	•	-
Performances	Max. width [in k pixels]	4	8	8	8	16	16	16
	Max. height [in k lines]	8	4	4	4	16	16	16
Image Formats	Gray8 (8bit output)		•			•		
	Gray16 (16bit output)			•			•	
	RGB 24 (3x8bit output)	•						•

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Steubenstrasse 46 D-68163 Mannheim Fax: Germany

APPLETS

Phone: +49(0)621 789507-0 +49(0)621 789507-10

Email: info@silicon-software.de Web: www.silicon-software.de www. silicon-software.com

