# VC series User Manual



VC-2MC-150/340 VC-3MC-280 VC-4MC-80/180 VC-12MC-65 VC-25MC-30

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### **Revision History**

Revision	Date	Description		
1.0	2010-10-01	Initial release		
1.1	2010-12-13	Added "scl" and "gcl" command		
1.2	2010-12-21	Added VC-2MC-M/C340 model		
1.3	2011-04-07	Revised Max. Frame Rate		
1.4	2011-07-12	Added 2 Tap and 4 Tap 10 bit		
		Added VC-2MC-150 and VC-4MC-80		
		Added Camera Block Diagram description		
1.5	2011-10-25	Added Data Format		
		Added Defective Pixel Correction		
		Added Appendix A and B		
1.6	2011-11-11	Added VC-4MC-40		
1.7	2012-06-19	Added 8 Tap 10 bit data output to VC-2MC-340 and VC-4MC-180 models		
1.8	2012-08-27	Revised the description of the Get Trigger Mode command		
	2012-10-15	Changed the User Manual file name		
1.0		Deleted VC-4MC-40 model		
1.9		Added VC-3MC-280 model		
		Added VC-25MC-30 model		
2.0	2013-06-14	Added description of M5 set screws for tilt adjustment		
2.0		Added Actual Time Applied for Commands		
2.1	2013-09-09	Added VC-12MC-65		
2.2	2012 11 14	Added 2 Tap and 4 Tap output mode to VC-12MC-65		
Ζ.Ζ	2013-11-11	Revised the maximum FPS values depending on ROI Size of VC-2MC		
2.3	2014-07-25	Added the maximum FPS values depending on ROI Size of VC-4MC		
2.4	2014-09-19	Applied new CI		

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# **1** Precautions

#### General

	•	Do not drop, disassemble, repair or alter the device. Doing so may damage the camera
		electronics and cause an electric shock.
	•	Do not let children touch the device without supervision.
$\wedge$	•	Stop using the device and contact the nearest dealer or manufacturer for technical
CAUTION		assistance if liquid such as water, drinks or chemicals gets into the device.
	•	Do not touch the device with wet hands. Doing so may cause an electric shock.
	•	Do not store the device at a higher temperature. In addition, maintain the ambient
		temperature in a range of 0 $^\circ C$ to 40 $^\circ C$ during operation. Otherwise the device may be
		damaged by excessively high temperatures.

#### Installation & Maintenance

	• Do not install in dusty or dirty areas - or near an air conditioner or heater to reduce the
	risk of damage to the device.
	• Avoid installing and operating in an extreme environment where vibration, heat, humidity,
	dust, strong magnetic fields, explosive/corrosive mists or gases are present.
$\wedge$	• Do not apply excessive vibration and shock to the device. This may damage the device.
	Avoid direct exposure to a high intensity light source. This may damage the image
CAUTION	sensor.
	• Do not install the device under unstable lighting conditions. Severe lighting change will
	affect the quality of the image produced by the device.
	• Do not use solvents or thinners to clean the surface of the device. This can damage the
	surface finish.

#### **Power Supply**

	•	Applying incorrect power can damage the camera. If the voltage applied to the camera is
CAUTION		greater or less than the camera's nominal voltage, the camera may be damaged or
		operate erratically. Please refer to 5.2 Specifications for the camera's nominal voltage.
		※ Vieworks Co., Ltd. does NOT provide power supplies with the devices.
	•	Make sure the power is turned off before connecting the power cord to the camera.
		Otherwise, damage to the camera may result.

# 2 Warranty

For information about the warranty, please contact your local dealer or factory representative.

# 3 Compliance & Certifications

### 3.1 FCC Declaration

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at own expenses.

### 3.2 CE : DoC

EMC Directive 2004/108/EC. Testing Standard EN 55022:2006+A1:2007, EN 55024:1998+A1:2001+A2:2003 Class A

# 3.3 KC

#### **KCC Statement**

Туре	Description
Class A	This device obtained EMC registration for office use (Class A), and may
(Broadcasting Communication	be used in places other than home. Sellers and/or users need to take
Device for Office Use)	note of this.

### 4 Package Components



M5 Set Screws for Tilt Adjustment (Provided only with F-mount camera)



# **5 Product Specifications**

### 5.1 Overview

VC Series is a high speed industrial area scan camera equipped with the latest global shutter CMOS image sensor. With its high reliability and durability, this camera is best suitable for machine vision requiring high-speed continuous shooting.

#### Main features

- High speed 2/3/4/12/25 megapixel CMOS image sensor
- Electronic exposure time control (global shutter)
- Output Pixel Format
  - VC-2MC / 4MC / 12MC / 25MC: 8 / 10 bit
  - VC-3MC: 8 bit
- Strobe Output
- Defective Pixel Correction
- Output Channel
  - VC-2MC / 4MC / 12MC: 2 Tap, 4 Tap, 8 Tap, 10 Tap
  - VC-3MC / 25MC: 8 Tap, 10 Tap
- Gain/Offset Control
- Test Image
- LVDS (RS-644) serial communication by Camera Link interface
- Temperature monitor
- Field upgrade
- Dark image correction (Supported only on VC-2MC / 3MC / 4MC / 12MC)

### 5.2 Specifications

Specifications for each VC camera model are as follows.

VC Series	VC-2MC-150	VC-2MC-340	VC-3MC-280	
Resolution (H x V)	2048 >	2048 × 1088		
Sensor	CMOSIS	CMOSIS CMV 2000		
Sensor Size (mm²)	11.26 ×	5.98 (2/3")	13.57 × 13.68 (1″)	
Sensor Type	High Spee	d Progressive Scan CMOS In	nage Sensor	
Pixel size	5.5 µm >	× 5.5 μm	$8.0~\mu$ m $ imes~8.0~\mu$ m	
Interface		Camera Link		
Electronic Shutter		Global Shutter		
	2 Tap: 1	74.4 fps	2 Tap: N/A	
	4 Tap: 1	48.5 fps	4 Tap: N/A	
Max. Frame Rate	8 Tap: N/A	8 Tap: 295.4 fps	8 Tap: 227 fps	
	10 Tap: N/A	10 Tap: 337.6 fps	10 Tap: 284 fps	
	2 Tap: 1	3.44 ms	2 Tap: N/A	
<b>T</b>	4 Tap: (	6.73 ms	4 Tap: N/A	
Transfer Time	8 Tap: N/A	8 Tap: 3.38 ms	8 Tap: 4.41 ms	
	10 Tap: N/A	10 Tap: 2.96 ms	10 Tap: 3.51 ms	
Pixel Data Format	8 bit (2/4 Tap)	8 bit (2/4/8/10Tap)	8 hit (8/10Tan)	
	10 bit (2/4 Tap)	10 bit (2/4/8 Tap)	0.01 (0/10149)	
Pixel Clock	85 MHz			
Exposure Time	1/100000 ~ 7 sec (10 µs step)			
Cable Length	< 5 m(Camera Link Cable at 85 Mtz)			
Black Offset	0 ~ 63 LSB, 64 step			
Video Gain		0 ~ 12 dB, 64 step		
Trigger Mode	Free-Run, Trigger			
Programmable Exposure Time and Trigge		ger Polarity		
External Trigger	External, 3.3 V	/ ~ 5.0 V Logical level input, C	Optically isolated	
Software Trigger		Camera Link CC1		
Dynamic Range		60 dB		
Lens Mount	C or F-mount			
Power	10 ~ 14 V DC, Max. 6 W			
Environmental	Operating: 0°C ~ 40°C, Storage: -40°C ~ 70°C			
Mechanical	68 mm × 68 mm × 54 mm, 420g (with F-mount)			
Configuration SW		Configurator		
	Table 5.1 Specification	s of VC Series (VC-2MC / V	C-3MC)	

VC Series	VC-4MC-80	VC-4MC-180	VC-12MC-65	
Resolution (H x V)	2048 >	2048 × 2048		
Sensor	CMOSIS (	CMOSIS CMV 4000		
Sensor Size (mm <sup>2</sup> )	11.26 ×	11.26 (1″)	22.5 × 16.9	
			(Diagonal: 28.14 mm)	
Sensor Type	High Speed	Progressive Scan CMOS In	hage Sensor	
Pixel size		5.5 $\mu$ m $ imes$ 5.5 $\mu$ m		
Interface		Camera Link		
Electronic Shutter		Global Shutter		
	2 Tap: 3	9.6 fps	2 Tap: 13.0 fps	
Max Frama Pata	4 Tap: 7	′8.9 fps	4 Tap: 26.0 fps	
	8 Tap: N/A	8 Tap: 157.1 fps	8 Tap: 51.7 fps	
	10 Tap: N/A	10 Tap: 179.5 fps	10 Tap: 64.3 fps	
	2 Tap: 2	25.3 ms	2 Tap: 76.9 ms	
Transfer Time	4 Tap: 1	4 Tap: 38.5 ms		
	8 Tap: N/A	8 Tap: 6.37 ms	8 Tap: 19.4 ms	
	10 Tap: N/A	10 Tap: 5.58 ms	10 Tap: 15.6 ms	
Pivel Data Format	8 bit (2/4 Tap)	8 bit (2/4/8/10Tap)	8 bit (2/4/8/10 Tap)	
	10 bit (2/4 Tap)	10 bit (2/4/8 Tap)	10 bit (2/4/8 Tap)	
Pixel Clock				
Exposure Time	1/100000 ~ 7 sec	5/100000~7sec(10 µs step)		
Cable Length	< 5	m (Camera Link Cable at 85	MHz)	
Black Offset	0 ~ 63 LSB, 64 step			
Video Gain	0 ~ 12 dB, 64 step			
Trigger Mode	Free-Run, Trigger			
External Trigger	External 3.3 V = 5.0 V Logical level input. Optically isolated			
Softwara Trigger				
Dynamic Range 60 dB		I		
Lens Mount	C or F-mount		F-mount	
Power	10 ~ 14 V DC, Max. 6 W 10 ~ 14 V DC, Max. 6 W		10 ~ 14 V DC, Max. 6 W	
Environmental	Operating: 0℃ ~ 40℃, Storage: -40℃		C ~70℃	
Mechanical	68 mm $ imes$ 68 mm $ imes$ 54 mm, 420g (with F-mount)			
Configuration SW	Configurator			

 Table 5.2
 Specifications of VC Series (VC-4MC / VC-12MC)

VC Series	VC-25MC-30		
Resolution (H x V)	5120 × 5120		
Sensor	On Semiconductor VITA-25K		
Sensor Size (mm²)	23.04 $\times$ 23.04 (Diagonal: 32.5 mm)		
Sensor Type	High Speed Progressive Scan CMOS Image Sensor		
Pixel size	4.5 $\mu$ m $ imes$ 4.5 $\mu$ m		
Interface	Camera Link		
Electronic Shutter	Global Shutter		
	2 Tap: N/A		
Max Erama Pata	4 Tap: N/A		
	8 Tap: 25.3 fps		
	10 Tap: 30.8 fps		
	2 Tap: N/A		
Transfor Timo	4 Tap: N/A		
	8 Tap: 39.42 ms		
	10 Tap: 32.47 ms		
Pixel Data Format	8 bit (8/10 Tap), 10 bit (8 Tap)		
Pixel Clock	85 MHz		
Exposure Time	1/100000 ~ 7 sec (10 µs step)		
Cable Length	< 5 m (Camera Link Cable at 85 $_{MHz}$ )		
Black Offset	0 ~ 63 LSB, 64 step		
Video Gain	0 ~ 12 dB, 64 step		
Trigger Mode	Free-Run, Trigger		
Extornal Triggor	Programmable Exposure Time and Trigger Polarity		
Softwara Trigger			
Lens Mount			
Power	10 ~ 14 V DC, Max. 8 W		
Environmental	Operating: $0^{\circ}$ ~ $40^{\circ}$ C, Storage: $-40^{\circ}$ C ~ $70^{\circ}$ C		
Mechanical	68 mm $ imes$ 68 mm $ imes$ 54 mm, 420g (with F-mount)		
Configuration SW	Configurator		

#### Table 5.3 Specifications of VC Series (VC-25MC)

### 5.3 Camera Block Diagram



Figure 5.1 Camera Block Diagram

All controls and data processing of VC cameras are carried out in one FPGA chip. The FPGA generally consists of a 32 bit RICS Micro-Controller and Processing & Control Logic. The Micro-Controller receives commands from the user through the Camera Link interface and then processes them. The Processing & Control Logic processes the image data received from the CMOS sensor and then transmits data through the Camera Link interface. And also, the Processing & Control Logic controls the trigger inputs and strobe outputs which are sensitive to time. Furthermore, SDRAM and FLASH is installed outside FPGA. SDRAM is used for the frame buffer to process images and FLASH contains the firmware that operates the Micro-Controller.

### 5.4 Sensor Information

The following graphs show the spectral response for VC-2MC monochrome and color camera.



Figure 5.2 Mono Spectral Response for VC-2MC



Figure 5.3 Color Spectral Response for VC-2MC



The following graph shows the spectral response for VC-3MC monochrome and color camera.

Figure 5.4 Mono and Color Spectral Response for VC-3MC



The following graphs show the spectral response for VC-4MC monochrome and color camera.

Figure 5.5 Mono Spectral Response for VC-4MC



Figure 5.6 Color Spectral Response for VC-4MC



The following graph shows the spectral response for VC-12MC monochrome and color camera.

Figure 5.7 Mono and Color Spectral Response for VC-12MC



The following graph shows the spectral response for VC-25MC monochrome and color camera.

Figure 5.8 Mono and Color Spectral Response for VC-25MC

### 5.5 Mechanical Specification

The camera dimensions in millimeters are as shown in the following figures.



Figure 5.9 VC-2MC / 3MC / 4MC Camera Link C-mount Mechanical Dimension



for Camera Mounting

Figure 5.10 VC-2MC / 3MC / 4MC Camera Link F-mount Mechanical Dimension



Figure 5.11 VC-12MC Camera Link F-mount Mechanical Dimension



Figure 5.12 VC-25MC Camera Link F-mount Mechanical Dimension

# 6 Connecting the Camera

The following instructions assume that you have installed a Camera Link frame grabber in your PC including related software. For more information, refer to your Camera Link frame grabber User Manual. To connect the camera to your PC, follow the steps below:

- 1 Make sure that the power supply is not connected to the camera and your PC is turned off.
- 2 Plug one end of a Camera Link cable into the Camera Link connector on the camera and the other end of the Camera Link cable into the connector on your Camera Link frame grabber.
- 3 Connect the plug of the power adaptor to the power input connector on the camera.
- 4 Plug the power adaptor into a working electrical outlet.
- 5 Verify that all the cable connections are secure.

### 6.1 Mount Plate



- The Mount Plate is provided as an optional item.
- The camera can be fixed without using this Mount Plate.

### 6.2 Precaution to center the image sensor

- User does not need to center the image sensor as it is adjusted as factory default settings.
- When you need to adjust the center of image sensor, please contact your local dealer or the manufacturer for technical assistance.

### 6.3 Precaution about blurring compared to the center

- User does not need to adjust the tilt as it is adjusted as factory default settings.
- If the tilt settings need to be adjusted inevitably, please contact your local dealer or factory representative for technical support.

### 6.4 Controlling the Camera

- You can control the camera by executing the Configurator.exe file.
- You can download the latest Configurator at machinevision.vieworks.com.
- Please refer to your Camera Link frame grabber user manual.

# 7 Camera Interface

### 7.1 General Description

As shown in the following figure, 4 types of connectors and status indicator LED are located on the back of the camera and have the functions as follows:

- (1) 26 pin Camera Link Connector 1 (Base):
- controls video data transmission and the camera.
- ② 26 pin Camera Link Connector 2 (Medium/Full): transmits video data
- ③ Status LED:
- ④ 6 pin Power Input Connector:
- (5) 4 pin Control Connector:

supplies power to the camera.

displays power status and operation mode.

inputs external trigger signal and outputs strobe.



Figure 7.1 VC Series Back Panel

### 7.2 Camera Link Connector



Figure 7.2 Camera Link Connector

Camera output complies with Camera Link Standard and the following list shows the pin configuration of the connector.

PAIR List	Pin	Signal Name	Туре	Description
	1	Ground	Ground	Cable Shield
PAIR 0	14	Ground	Ground	Cable Shield
	2	-X0	LVDS - Out	Camera Link Transmitter
PAIR I	15	+X0	LVDS - Out	Camera Link Transmitter
PAIR 2	3	-X1	LVDS - Out	Camera Link Transmitter
	16	+X1	LVDS - Out	Camera Link Transmitter
PAIR 3	4	-X2	LVDS - Out	Camera Link Transmitter
	17	+X2	LVDS - Out	Camera Link Transmitter
PAIR 4	5	-XCLK	LVDS - Out	Camera Link Transmitter
	18	-XCLK	LVDS - Out	Camera Link Transmitter
PAIR 5	6	-X3	LVDS - Out	Camera Link Transmitter
	19	+X3	LVDS - Out	Camera Link Transmitter
	7	+ SerTC	LVDS - In	Serial Data Receiver
PAIR 0	20	- SerTC	LVDS - In	Serial Data Receiver
	8	- SerTFG	LVDS - Out	Serial Data Transmitter
	21	+ SerTFG	LVDS - Out	Serial Data Transmitter
	9	- CC 1	LVDS - In	Software External Trigger
PAIR 0	22	+ CC 1	LVDS - In	Software External Trigger
	10	N/C	N/C	N/C
PAIR 9	23	N/C	N/C	N/C
	11	N/C	N/C	N/C
PAIR 10	24	N/C	N/C	N/C
	12	N/C	N/C	N/C
PAIK II	25	N/C	N/C	N/C
	13	Ground	Ground	Cable Shield
PAIR 12	26	Ground	Ground	Cable Shield

 Table 7.1
 Pin Assignments for Camera Link Connector 1

PAIR List	Pin	Signal Name	Туре	Description
	1	Ground	Ground	Cable Shield
PAIR U	14	Ground	Ground	Cable Shield
	2	-Y0	LVDS - Out	Camera Link Transmitter
PAIR	15	+Y0	LVDS - Out	Camera Link Transmitter
PAIR 2	3	-Y1	LVDS - Out	Camera Link Transmitter
	16	+Y1	LVDS - Out	Camera Link Transmitter
PAIR 3	4	-Y2	LVDS - Out	Camera Link Transmitter
	17	+Y2	LVDS - Out	Camera Link Transmitter
PAIR 4	5	-YCLK	LVDS - Out	Camera Link Transmitter
	18	+YCLK	LVDS - Out	Camera Link Clock Tx
PAIR 5	6	-Y3	LVDS - Out	Camera Link Channel Tx
	19	+Y3	LVDS - Out	Camera Link Channel Tx
	7	-	Not Used	Connected with 100 chm
PAIR 0	20	-	Not Used	Connected with 100 onin
	8	-Z0	LVDS - Out	Camera Link Transmitter
	21	+Z0	LVDS - Out	Camera Link Transmitter
	9	-Z1	LVDS - Out	Camera Link Transmitter
PAIR O	22	+Z1	LVDS - Out	Camera Link Transmitter
	10	-Z2	LVDS - Out	Camera Link Transmitter
FAIR 9	23	+Z2	LVDS - Out	Camera Link Transmitter
	11	-ZCLK	LVDS - Out	Camera Link Transmitter
PAIR IU	24	+ZCLK	LVDS - Out	Camera Link Clock Tx
	12	-Z3	LVDS - Out	Camera Link Channel Tx
	25	+Z3	LVDS - Out	Camera Link Channel Tx
	13	Ground	Ground	Cable Shield
PAIK 12	26	Ground	Ground	Cable Shield

 Table 7.2
 Pin Assignments for Camera Link Connector 2

Model	Camera-Link Output Mode	CL Configuration	CL Connector 1	CL Connector 2
	2 Тар	BASE	0	Х
	4 Tap	MEDIUM	0	0
VC-4IMC-180	8 Tap	FULL	0	0
VC-12IVIC-05	10 Tap	FULL	0	0
VC-3MC-280	8 Tap	FULL	0	0
VC-25MC-30	10 Tap	FULL	0	0

 Table 7.3
 Connector Arrangement for Camera Link Output Modes



When you connect a Frame Grabber to Camera Link Connectors using Camera Link cables, make sure you connect to the correct Camera Link Connector. Incorrect connection of Connector 1 and Connector 2 may cause malfunction of the camera or communication problems between PC and the camera.

### 7.3 Power Input Connector

The power input connector is a Hirose 6 pin connector (part # HR10A-7R-6PB). Pin arrangement and configuration are as follows:



Figure 7.3 Pin Arrangement of Power Input Connector

Pin Number	Signal	Туре	Description
1, 2 , 3	+ 12V DC	Input	DC Power Input
4 , 5 , 6	DC Ground	Input	DC Ground

 Table 7.4
 Pin Configuration of Power Input Connector

Connecting the power cable to the camera can be made by using the Hirose 6 pin plug (part # HR10A-7P-6S) or the equivalent. The power adaptor is recommended to have at least 1A current output at  $12 \text{ V DC} \pm 10\%$  voltage output (Users need to purchase the power adaptor separately).

#### **Precaution for Power Input**

$\land$	•	Make sure the power is turned off before connecting the power cord to the camera.
		Otherwise, damage to the camera may result.
CAUTION	•	If the camera input voltage is greater than 16 V, damage to the camera may result.

### 7.4 Control Connecter

The control connector is a Hirose 4 pin connector (part # HR10A-7R-4S) and consists of an external trigger signal input and strobe output port. Pin arrangement and configuration are as follows:



Figure 7.4 Pin Arrangement of Control Connector

Pin Number	Signal	Туре	Description
1	Trigger Input +	Input	-
2	Trigger Input -	Input	-
3	DC Ground	-	DC Ground
4	Strobe Out	Quitout	3.3 V TTL Output
	Strobe Out	Ουιραι	Output resistance : 47 $\Omega$

 Table 7.5
 Pin Arrangement of Control Connector

The mating connector is a Hirose 4 pin plug (part # HR10A-7P-4P) or the equivalent connectors.

### 7.5 Trigger Input Circuit

Following figure shows trigger signal input circuit of the 4-pin connector. Transmitted trigger signal is applied to the internal circuit through a photo coupler. Minimum trigger width that can be recognized by the camera is 1  $\mu$ s. If transmitted trigger signal is less than 1  $\mu$ s, the camera will ignore the trigger signal. External trigger circuit example is shown below.



Figure 7.5 Trigger Input Schematic



Figure 7.6 Recommended Pulse Trigger Driver Input



Figure 7.7 Recommended Contact Trigger Input

### 7.6 Strobe Output Circuit

The strobe output signal is 3.3 V output level of a TTL Driver IC. The pulse width of signal is synchronized with the exposure signal (shutter) of the camera.



Figure 7.8 Strobe Output Schematic

# 8 Camera Features

### 8.1 Region Of Interest (ROI)

The Region of Interest (ROI) feature allows you to specify a portion of the sensor array. You can acquire only the frame data from the specified portion of the sensor array while preserving the same quality as you acquire a frame from the entire sensor array. ROI is determined as the overlapping area of two areas when designating start point and end point in horizontal and vertical direction as shown in the figure below. Start point and End point mean the starting and end of the ROI. In the VC-2MC, 4MC and 12MC models, the frame rate will increase as the vertical ROI is made smaller. However, the horizontal ROI does not affect the frame rate. In the VC-3MC and VC-25MC models, the frame rate will increase as both the vertical and horizontal ROI are made smaller.



In VC-2MC, the maximum frame rates depending on the changes of vertical ROI are shown in the following table.

ROI Size (H $\times$ V)	2 Тар	4 Tap	8 Тар	10 Тар
2048 × 16	3689.3 fps	6692.2 fps	10622.3 fps	12103.0 fps
2048 × 32	2094.4 fps	4036.9 fps	6979.1 fps	7960.3 fps
2048 × 64	1123.3 fps	2250.8 fps	4139.6 fps	4725.4 fps
2048 × 128	582.8 fps	1194.1 fps	2282.4 fps	2606.7 fps
2048 × 256	297.0 fps	615.9 fps	1203.0 fps	1374.3 fps
2048 × 512	149.9 fps	312.9 fps	618.2 fps	706.4 fps
2048 × 1024	75.3 fps	157.7 fps	313.5 fps	358.2 fps

 Table 8.1
 VC-2MC Maximum Frame Rate depending on ROI Size

In VC-3MC, the maximum frame rates depending on the changes of both horizontal and vertical ROI are shown in the following table.

ROI Size (H $ imes$ V)	8 Tap	10 Тар
640 × 480	1830 fps	2288 fps
1024 × 768	807 fps	1010 fps
1280 × 1024	486 fps	632 fps
1600 × 1200	346 fps	432 fps
1680 × 1680	229 fps	296 fps
1696 × 1710	227 fps	284 fps

Table 8.2 VC-3MC Maximum Frame Rate depending on ROI Size

In VC-4MC, the maximum frame rates depending on the changes of vertical ROI are shown in the following table.

ROI Size (H $\times$ V)	2 Tap	4 Tap	8 Тар	10 Тар
2048 × 16	3689.3 fps	6692.2 fps	10622.3 fps	12103.0 fps
2048 × 32	2094.4 fps	4036.9 fps	6979.1 fps	7960.3 fps
2048 × 64	1123.3 fps	2250.8 fps	4139.6 fps	4725.4 fps
2048 × 128	582.8 fps	1194.1 fps	2282.4 fps	2606.7 fps
2048 × 256	297.0 fps	615.9 fps	1203.0 fps	1374.3 fps
2048 × 512	149.9 fps	312.9 fps	618.2 fps	706.4 fps
2048 × 1024	75.3 fps	157.7 fps	313.5 fps	358.2 fps
2048 × 2048	39.6 fps	78.9 fps	157.1 fps	179.5 fps

Table 8.3 VC-4MC Maximum Frame Rate depending on ROI Size

In VC-12MC, the maximum frame rates depending on the changes of vertical ROI are shown in the following table.

ROI Size (H $\times$ V)	2 Tap	4 Tap	8 Tap	10 Тар
4096 × 500	78 fps	156 fps	304.1 fps	377.9 fps
4096 × 1000	39 fps	79 fps	155.8 fps	193.6 fps
4096 × 1500	26 fps	52 fps	104.7 fps	130.1 fps
4096 × 2000	19 fps	39 fps	78.9 fps	98.0 fps
4096 × 2500	15 fps	31 fps	63.2 fps	78.6 fps
4096 × 3072	13 fps	25 fps	51.6 fps	64.0 fps

#### Table 8.4 VC-12MC Maximum Frame Rate depending on ROI Size

In VC-25MC, the maximum frame rates depending on the changes of both horizontal and vertical AOI are shown in the following table.

AOI Size (H $ imes$ V)	8 Tap	10 Тар
3000 × 3000	57.0 fps	69.2 fps
4000 × 3000	49.3 fps	59.9 fps
4000 × 4000	37.0 fps	45.0 fps
5120 × 5120	25.3 fps	30.8 fps

 Table 8.5
 VC-25MC Maximum Frame Rate depending on ROI Size

	•	The ROI values (H $ imes$ V) may vary depending on the type of Camera Link frame
$\wedge$		grabber. For technical assistance, contact your local dealer or the manufacturer.
	•	The ROI setting commands for VC-12MC are different from the other models since it
CAUTION		supports the Multiple ROI feature. Refer to Table 9.4 Command List #4 for the related
		commands.

### 8.2 Multi-ROI (VC-12MC Only)

The VC-12MC-65 model provides the Multi-ROI feature which allows you to define up to thirty two regions on the sensor array. When an image is acquired, only the pixel information from the defined regions will be readout of the sensor. The pixel data read out of the regions will then be combined together and will be transmitted from the camera as a single image. It is recommended that you first set the **Offset X** and **Width** parameters, since all of the regions must be the same width and all of the regions must be vertically aligned. The next step in the setup process is to define each individual region in order. Up to 32 regions can be set up ranging from 0 through 31. Set the each region to ON/OFF and then set the **Offset Y** (the offset between the top of the sensor and the top of the region) and **Height** (the height of the region) values to define each region. In figure 8.2, for example, three regions have been set. With these settings, the camera would output an image with 2048 (width)  $\times$  1844 (the total height of the three regions) size.

- Offset X = 1024, Width = 2048
- ROI\_0
  - Diffset Y = 204, Height = 512
- ROI\_1
  - Offset Y = 1024, Height = 716
- ROI\_2
  - Diffset Y = 2356, Height =616



Figure 8.2 Multi-ROI
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There are several things to keep in mind when setting Multi-ROI:

- The sum of the Offset X value plus the Width value must not exceed the width (4096 on the VC-12MC) of the camera's sensor.
- The sum of the Offset Y value plus the Height value must not exceed the height (3072 on the VC-12MC) of the camera's sensor.
- The Offset X and Width values must be a multiple of 8 ranging from 24 to 4096.
- The Offset Y and Height values must be a multiple of 4 ranging from 4 to 3072.
- You need to set the regions in order. When you set a preceding region to OFF, all the succeeding regions will be set to OFF automatically. For example, you have set four regions from 0 through 4. If you set the region 1 to OFF, the region 2, 3 and 4 will be set to OFF.
- You do not need to order the regions from top to bottom on the sensor. For example, you could place the region 1 near the bottom of the sensor, the region 2 near the top, and the region 3 in the middle.
- You can save (Configurator > File > Save Setting > User 1 or User 2) the setting values for Multi-ROI to parameter storage space and then load (Configurator > File > Load Setting > From User 1 Space or From User 2 Space) the values to the camera when desired.
- Refer to <u>Table 9.4 Command List #4</u> for the commands related to Multi-ROI.

# 8.3 Binning (VC-25MC Only)

Binning has the effects of increasing the level value and decreasing resolution by adding the values of the adjacent pixels and sending them as one pixel. VC-25MC provides two binning factor ( $\times$ 1 and  $\times$ 2) that the user can apply both horizontally and vertically. The below figure shows application of 2  $\times$  2 binning, four pixels are summed and reported out of the camera as a single pixel. Using binning reduces the resolution of camera's imaging sensor in half, however, it results in double signal to noise ratio with the same brightness as an original image. You can set the Binning Factor by using the 'sbf' command.



Figure 8.3  $2 \times 2$  Binning



Even if the binning is performed on the color camera, the resulting image will be monochrome.

## 8.4 Exposure

The CMOS sensor of VC series uses global shutter that exposes the entire imager simultaneously. The below figure illustrates the timing of exposure and readout of CMOS sensor. Readout is performed consecutively from first to last line where readout defines the process of reading the accumulated charges on pixels. Readout Time (also called Transfer Time) defines the rate at which one frame of an image is transferred.



Figure 8.4 Exposure Timing Diagram

## 8.4.1 Real Exposure (VC-12MC Only)

In VC-12MC-65, when you set the Trigger Mode to Free-Run or the Exposure Mode to Program, the exposure time will be determined by the user settings ('set' command). With these settings, the camera will generate trigger signals internally. As soon as the camera detects a trigger signal, the camera will begin a frame exposure. When the exposure time ends, the pixels are being sampled and prepared for readout. This sequence is called the frame overhead time (FOT). Immediately after the FOT, the frame is read out automatically. During the FOT, an additional exposure time will be applied as the offset value shown in the Figure 8.5. This offset value varies depending on the camera's output channel settings. The setting value on the exposure time is equal to actual exposure time, because the offset value is compensated on the actual exposure time. In 8 tap output channel mode, for example, the exposure time is set to 200  $\mu$ s. The camera will subtract offset value 50  $\mu$ s from the exposure time setting internally, and thus the actual exposure time will be 200  $\mu$ s as the user settings. Due to the offset value, the minimum exposure time is limited as shown in the Table 8.6.



#### Figure 8.5 Real Exposure for VC-12MC-65

Output Channel	Offset Value
2 Tap	50 µs
4 Tap	50 µs
8 Tap	50 µs
10 Tap	40 µs

Table 8.6 Offset Value for VC-12MC-65

Output Channel	Minimum Exposure Time
2 Tap	50 µs
4 Тар	50 µs
8 Тар	50 µs
10 Tap	40 µs

#### Table 8.7 Minimum Exposure Time for VC-12MC-65

## 8.5 Trigger Mode

Trigger mode of the camera is divided into Free-Run mode where image is synchronized to Internal Trigger signal created inside the camera and External Sync mode where image is synchronized to the trigger signal entered into the external port.

### 8.5.1 Free-Run Mode

In Free-Run mode, the period of internal trigger signal is determined by Transfer Time (1 Frame data transmission time) and Exposure setting value, and image is obtained with this periodic signal. Frame Rate, the period of internal signal, is determined with the following two conditions.

- Case 1: Exposure Time < Frame Transfer Time
  - Frame Rate(FPS) = 1 / Frame Transfer Time (sec)  $\rightarrow$  has a fixed value.
- Case 2: Exposure Time > Frame Transfer Time
  - Frame Rate(FPS) = 1 / Exposure Time (sec)  $\rightarrow$  varies depending on Exposure Time value.







#### Figure 8.7 Exposure Time is longer than Readout Time

### 8.5.2 External Sync Mode

In External Sync Mode, camera keeps standby status until trigger signal is entered, and when trigger is entered, the image is transmitted (frame transfer) after exposure process. To operate the camera in External Sync mode, it is required to set Trigger Source regarding which input, CC1 input port or External Trigger port, will be used for trigger signal, as well as Polarity and Exposure source of the signal entered.



#### Figure 8.8 External Sync Mode

Following is the summary of basic setting items.

- Trigger Source: select either CC1 (Camera Control Port 1) or External Connector as source of the external trigger input signal.
- Trigger Polarity: set whether polarity of Trigger signal entered is Active High or Active Low.
- Exposure Source: select to synchronize exposure time with pulse width of trigger input signal or with exposure time programmed inside the camera.

### 8.5.3 Overlap Trigger Input

When trigger is entered during frame transfer as shown in the figure below, it performs exposure process for newly entered trigger while transferring the previous data. In this case, shooting images is possible up to the maximum frame rate (speed of 1/Transfer Time (sec)) regardless of the exposure time.





Following list shows the operation of camera on exceptional trigger input.

- When the trigger signal with the faster period than the maximum frame rate condition is entered, the next frame transfer is performed before completing the previous frame transfer. This causes the failure of obtaining overall image.
- If new trigger is entered during the exposure process after Exposure Source is set as Program, the newly entered signal will be ignored. In this case, exposure time is longer than trigger input period and the frame rate will be slower than trigger input period since all the entered trigger signals cannot synchronize.

## 8.6 Camera Link Output

VC Series supports 2 Tap, 4 Tap, 8 Tap or 10 Tap output modes<sup>†</sup> according to the user interface. Tap setting value defines the number of output pixel data for each Pixel Clock Cycle (85 MHz), and the speed of frame data transmission varies depending on the tap setting. Frame Data is output in interleaved order as shown in the figure below. This tap setting can be set using the 'scl' command.



\*1 : Camera-Link Configuration





## 8.7 Gain and Offset

Gain and Offset can be changed through Voltage Reference adjustment which is applied commonly to all ADCs. Gain adjustment can be set among 0 ~ 12 dB and setting value can be set as a value from 0 to 64 step. Relation between setting value and actual Gain (dB) is shown below:

Gain(dB) = (setting value)  $\times$  0.19 dB



Figure 8.11 Register Setting vs Gain

Offset can be set between 0 ~ 63 (LSB) based on 8 bit data output and setting value has 64 step values.

## 8.8 Defective Pixel Correction

The CMOS sensor may have Defect Pixels which cannot properly react to the light. Correction is required since it may deteriorate the quality of output image. Defect Pixel information of CMOS used for each camera is entered into the camera during the manufacturing process. If you want to add Defect Pixel information, it is required to enter coordinate of new Defect Pixel into the camera.

For more information, refer to <u>Appendix A</u>. The 'sdc' command is used to set whether to use Defective Pixel Correction feature.

### 8.8.1 Correction Method

Correction value for Defect Pixel is calculated based on valid pixel value adjacent in the same line.



Figure 8.12 Location of Defect Pixel to be corrected

If Current Pixel is a Defect Pixel as shown in the above figure, correction value of this pixel is obtained as shown in the following table depending on whether surrounding pixel is Defect Pixel or not.

Adjacent Defect Pixel(s)	Correction value of Current Pixel		
None	(L1 + R1) / 2		
L1	R1		
R1	L1		
L1, R1	(L2 + R2 ) / 2		
L1, R1, R2	L2		
L2, L1, R1	R2		
L2, L1, R1, R2	(L3 + R3) / 2		
L2, L1, R1, R2, R3	L3		
L3, L2, L1, R1, R2	R3		

 Table 8.8
 Calculation of Defect Pixel Correction Value

## 8.9 Flat Field Correction (VC-12MC Only)

The Flat Field Correction feature improves the image uniformity when you acquire a non-uniformity image due to external conditions. The Flat Field Correction feature can be summarized by the following equation:

```
IC = (IR - IB) / (IF - IB)
Where,
IC : Level value of corrected image;
IR : Level value of original image;
IB : Black offset value;
IF : Level value of Flat Field data.
```

In order to use the Flat Field Correction feature, one must first generate IF, the Flat Field data. This can be done by adjusting the camera to the actual environment and activating the Flat Field Generator. The Flat Field Generator will standardize a series of images, curtailing the image to 1/16 pixel, generate the curtailed Flat Field data, and store it in the internal frame buffer of the FPGA. When curtailed images are used for corrections, it is expanded and applied with a Bilinear Interpolation as shown in the Figure 8.14. Use 'gfd' command to generate the Flat Field data and use the 'sfc' command to apply the Flat Field Correction. At this time, the Flat Field data is stored on the RAM, a volatile memory. In order to reuse the stored data, use 'sdf' command to store them on the FLASH, a non-volatile memory.

It is recommended that you enable the Defective Pixel Correction feature before activating the Flat Field Generator.
 Activating the Flat Field Generator will set the camera's resolution to its full resolution.
 To acquire an image, operate the camera with free-run mode or apply a trigger signal.
 When the generation of the Flat Field data is completed, the previous settings for the camera will be restored.



Figure 8.13 Generation and Application of Flat Field Data



Figure 8.14 Bilinear Interpolated Magnification

## 8.10 Auto White Balance (VC-12MC Only)

The Auto White Balance feature is implemented on the VC-12MC-C65 color camera. It will control the white balance of the image acquired from the color camera according to the GreyWorld algorithm. The entire pixel data will be used by the Auto White Balance feature to control the white balance. As soon as you activate the Auto White Balance feature, the Digital Red, Digital Green and Digital Blue will be set to 1. Then the Digital Red and Digital Blue will be adjusted to control the white balance. The 'arg' command is used to set whether to use the Auto White Balance feature.

## 8.11 Temperature Monitor

The camera has an embedded sensor chip to monitor the internal temperature. The 'gct' command is used to check the temperature of the camera.

## 8.12 Status LED

•

There is a green LED to inform the operation status on the back of camera. LED status and corresponding camera status are as follows:

- Continuous ON: operates in Free-Run Mode.
- Repeat ON for 0.5 second, OFF for 0.5 second:
  - Repeat ON for 1 second, OFF for 1 second: outputs Test Image.
- Repeat ON for 0.25 second, OFF for 0.25 second: operates in Trigger Mode and outputs Test Image.

operates in Trigger Mode.

## 8.13 Data Format

Data can be processed in the unit of 10 bit internally, but can be selectively output in the unit of 8 or 10 bit<sup>†</sup> at output. When it is output in 8 bit unit, the 2 least significant bits will be dropped from overall 10 bits.







#### † Supported data format

VC-3MC-280 supports only 8 bit.

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## 8.14 Test Image

To check normal operation of the camera, it can be set to output test image created inside, instead of image data from CCD. There are 3 types of test image; image with different value in horizontal direction (Test Image 1), image with different value in diagonal direction (Test Image 2), and moving image with different value in diagonal direction (Test Image 3). Test image can be applied in all operation modes of the camera. To set Test Image, use the 'sti' command.



Figure 8.16 Test Image 1



Figure 8.17 Test Image 2



Figure 8.18 Test Image 3

## 8.15 Strobe

Strobe signal is used to synchronize the external light source with camera or to measure the exposure time applied to current camera. Pulse width of Strobe signal is from the generating point of Shutter signal to the starting point of Readout, which coincides with exposure time of camera.

## 8.15.1 Strobe Output

The camera can provide the Strobe Output signal that is synchronized with the exposure time of the camera. The Strobe Output signal is shown below.



Figure 8.19 Strobe signal in Trigger mode

### 8.15.2 Strobe Polarity

Polarity can be set for Strobe signal output. The 'ssp' command is used to set the polarity of strobe signal.

## 8.16 Field Upgrade

The camera has the function to upgrade firmware and FGPA logic through RS-644 interface of Camera Link, rather than disassemble the camera in the field. See <u>Appendix B</u> for details on how to upgrade.

## 8.17 Dark Image Correction

CMOS sensor may result in lower sensitivity at dark level. This is caused by fixed pattern noise variation depending on the exposure settings or characteristic change according to the temperature variation of AFE and sensor cell. Sensitivity change resulted by temperature variation is less than 1 dB/10 degree. The acquisition condition of correction data is 25 degree based on the camera case temperature. To acquire optimized image at user environment, it is recommended to perform dark image correction after the camera is installed and the temperature of the camera is stabilized.



VC-25MC-30 does not support dark image correction.

### 8.17.1 Sequence of Dark Image Correction

#### How to Correct Dark Image using Configurator

- 1. Prevent penetration of light into the camera image sensor.
- 2. Click the **Generate Data** button in the **View** tab to generate correction data.
- 3. Click the **Save Data** button to save correction data in the flash memory. The saved data will be applied to the camera automatically when the camera is turned on.



Figure 8.20 Dark Image Correction on Configurator

#### How to Correct Dark Image using Serial Command

- 1. Prevent penetration of light into the camera image sensor.
- 2. Use the terminal command 'gop' to generate correction data in the camera.
- 3. Use the terminal command 'sop' to save correction data in flash memory.

## 8.18 White Pixel

If you use a VC-25MC camera under the condition of high ambient temperature, white pixels (also known as hot pixels) may be occurred due to the characteristic of equipped high resolution CMOS sensor. White pixels are caused by accumulated current leakage in the charge storage region inside the imaging sensor's active pixel. If temperature is increased by seven degrees, it is getting worse with double white pixels. To effectively reduce white pixels, maintain the operating temperature as low as possible and mount the camera on a substantial metal component in your system to provide sufficient heat dissipation. You can also use the defective pixel correction to remove white pixels. Add a defective pixel to defective pixel map or modify the defective pixel map stored in the camera.



Figure 8.21 White Pixel

# 9 Camera Configuration

## 9.1 Setup Command

All the settings for the camera are carried out through RS-644 serial interface of camera link. With the following communication setting, it can be controlled using terminal or direct control at user application.

- Baud Rate: 19200 bps
- Data Bit: 8 bit
- Parity Bit: No Parity
- Stop bit: 1 stop bit
- Flow control: None

All types of camera setting commands except Firmware Download, requiring massive data transmission are delivered in ASCII command type. All camera setup commands start from user application and the camera returns the response ("OK", "Error" or information) for the command. The camera informs the completion of command execution through response with write command, while the camera returns the error response or information with read command.

```
Command format:
<command> <parameterl> <parameter2> <cr>
0~2 parameters follow the command.
Response:
- If execution of write command is successfully completed
OK <cr>> <lf>
```

ex) Write command

```
If execution of read command is successfully completed
<parameterl> <cr> <lf>
```

#### ex) Read command

```
In response to a "get" command the camera will return (in hex value)
Command : 67 65 74 0D
get <cr>
Response : 67 65 74 0D 0A 31 30 30 0D 0A 3E
get<cr><1f> 100<cr><1f> 2cho response prompt
```

If execution of command is not completed
Error : <Error Code> <cr>> <lf>

```
Prompt:
After sending response, Camera sends prompt always. `>'is used as prompt.
Types of Error Code
0x80000481 : values of parameter not valid
0x80000482 : number of parameter is not matched
0x80000484 : command that does not exist
0x80000486 : no execution right
```

## 9.2 Actual Time Applied for Commands

When you execute a command, the actual or real time applied for the command varies depending on the type of the command and operating status of the camera.

All commands except Set Exposure Time ('set') command are applied to change the settings as illustrated below, on the rising edge of a VCCD signal before starting readout process.

When you execute a 'set' command, the exposure time setting will be changed at the starting of the exposure. In the Trigger mode, you must execute commands before applying trigger signals in order to synchronize image outputs with the commands.

In the Free-Run mode, even if you execute a command, you may acquire up to two images without applying the command. This is true because it is hard to verify the current operating status of the camera in the Free-Run mode.



Figure 9.1 Actual Time Applied for Commands

## 9.3 Parameter Storage Space

The camera has 3 non-volatile storage space used for parameter storage and 1 volatile work space that is applied to actual camera operation. 3 storage space is divided into Factory Space that contain basic value at the factory, and 2 user space (User Space 1, User Space 2) that can save parameter value temporarily set by the user. User space can be read and written, but Factory space can be read only. At camera booting, setting value in one of 3 storage spaces is copied to work space according to Config Initialization value and value of the space is used for camera setting. Since values in work space is valid only

while the power is on, it should be copied to user space 1 or user space 2 using the 'sct' command.



Figure 9.2 Parameter Area

# 9.4 Command List

Command	Syntax	Value Returned	Description
Help	h	String	Displays a list of all commands
			0 : Normal Mode
Set Read-Out Mode	srm 0 1	ОК	1 : ROI (Region of Interest) Mode
Get Read-Out Mode	grm	0 1	(ROI area is set using 'sha' and 'sva'
			commands.)
Set Horizontal Area	sha n1 n2	OK	n1: Starting point of horizontal direction
Get Horizontal Area	gha	n1 n2	n2 : End point of horizontal direction
Set Vertical Area	sva n1 n2	ОК	n1 : Starting point of vertical direction
Get Vertical Area	gva	n1 n2	n2 : End point of vertical direction
Set Trigger Mode	stm 0 1	ОК	0 : Free Run Mode
Get Trigger Mode	gtm	0 1	1 : Trigger/Overlap Mode
Set Trigger Source	sts 1 2	ОК	1 : CC1 Port Input
Get Trigger Source	gts	1 2	2 : External Input
Set Trigger Polarity	stp 0 1	ОК	0 : Active Low
Get Trigger Polarity	gtp	0 1	1 : Active High
Set Exposure Source	ses 0 1	ОК	0 : Program Exposure (by camera)
Get Exposure Source	ges	0 1	1 : Pulse Width (by external trigger input)
Set Exposure Time	set n	OK	n : Exposure Time in $\mu$ s
Get Exposure Time	get	Ν	(Setting range : 10 ~ 7,000,000 µs)
Set Analog Gain	sag n	OK	n :Analog Gain Parameter
get Analog Gain	gag	Ν	(Setting Range : 0 ~ 63)
Set Analog Offset	sao n	ОК	n :Analog Offset Parameter
get Analog Offset	gao	N	(Setting Range : 0 ~ 63)
Set Test Image	sti 0 1 2 3	ОК	0 : OFF
Get Test Image	gti	0 1 2 3	1, 2 : Fixed Pattern Image
			3 : Moving Pattern Image
Set Data Bit	sdb 8 10	ОК	8 : 8 Bit Output
Get Data Bit	gdb	8 10	10 : 10 Bit Output
Set Strobe Polarity	ssp 0 1	ОК	0 : Active Low
Get Strobe Polarity	gsp	0 1	1 : Active High

#### Table 9.1 Command List #1

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Command	Syntax	Value Returned	Description
Generate Offset Calibration	god	ОК	Generate offset Calibration Data to the volatile
Data			Memory
Save Offset Calibration	sod	ОК	Save offset Calibration Data to the Flash
Data			Memory
Load Offset-Calibration	Lod	OK	Load offset Calibration Data from the Flash
Data			Memory (Calibration data is loaded
Cat Defect Correction			
Set Defect Correction	sac UI1	UK	U: UT
Get Defect Correction	gdc	0 1	1 : Active of Defect Correction
			0 : 2 Tap Output
Set Camera-Link Mode	scl 0 1 2 3	ОК	1 : 4 Tap Output
Get Camera-Link Mode	gcl	0 1 2 3	2 : 8 Tap Output
			3 : 10 Tap Output
Set Binning Factor	sbf 1 2	ОК	1: binning off
Get Binning Factor	gbf	1 2	2: 2 by 2 binning

### Table 9.2 Command List #2

Command	Syntax	Value Returned	Description
Save Config To	sct 1 2	ОК	1 : Save to User 1 Setting
			2 : Save to User 2 Setting
Load Config From	lcf 0 1 2	ОК	0 : Load from Factory Setting
			1 : Load from User 1 Setting
			2 : Load from User 2 Setting
Set Config Initialization	sci 0 1 2	ОК	0 : Load from Factory Setting when initializing
Get Config Initialization	gci	0 1 2	1 : Load from User 1 Setting when initializing
			2 : Load from User 2 Setting when initializing
Get Model Name	gmn	String	Displays Model Name
Get MCU Version	gmv	String	Displays MCU Version
Get FPGA Version	gf∨	String	Displays FPGA Version
Get Serial Number	gsn piece	String	Display Serial Number
Get Current Temperature	gct	String	Display Temperature Value

#### Table 9.3 Command List #3

### VIEWORKS

Command	Syntax	Value Returned	Description
Set Region Select	srs n	ОК	n · Region number(0 - 31)
Get Region Select	grs	n	
Set Region Mode			
(current region)	src 0 1	ОК	0 : Region OFF
Get Region Mode	grc	0 1	1 : Region ON
(current region)			
Set Offset X	sox n	ОК	n - V avia affect
Get Offset X	gox	n	
Set Offset Y			
(current region)	soy n	ОК	
Get Offset Y	goy	n	n : Y axis onset
(current region)			
Set Width	siw n	ОК	n (Midth volue
Get Width	giw	n	
Set Height			
(current region)	sih n	ОК	
Get Height	gih	n	
(current region)			
Generate Flat-Field Data	gfd	ОК	
Save Flat-Field Data	sfd	ОК	0 - Flat Field Correction ON
Load Flat-Field Data	lfd	ОК	1 : Flat Field Correction ON
Set Flat-Field Correction	sfc 0 1	ОК	1 : Flat Field Correction OFF
Get Flat-Field Correction	gfc	0 1	
Set RGB Gain	srg r b f	ОК	
Get RGB Gain	grg r∣b	f	f : Gain Parameter
Auto generation RGB Gain	arg	ОК	

Table 9.4 Command List #4 (VC-12MC Only)

# **10 Configurator GUI**

Configurator, a sample application, is provided to control VC Series camera. Configurator provides easy-to-use Graphic User Interface (GUI) for the user while using the commands mentioned previous chapters.

## 10.1 Camera Scan

When you execute the program while the camera is turned on, Camera Scan window appears as shown in the figure below. At this time, the program checks serial port of computer and DLL provided by Camera Link to scan whether the camera is connected. If there is a camera connected, it displays model name on the screen. If the camera is not properly displayed on the screen, check the connection of cable with power of camera and press the **refresh** button. When you double-click model name displayed on the screen, Configurator is executed and displays current setting value of camera connected.



Figure 10.1 Configurator Loading Window

### 10.2 Menu

## 10.2.1 File

💿 Vieworks - VC-25M	1C-M30				
File Start-Up Tool	About				
Load Setting	۰.	From File			
Save Setting	×	From Factory Space			
Defect Pixel	•	From User1 Space			
System Upgrade	• •	From User2 Space			
Exit					
<ul> <li>● 1x1</li> <li>○ 2x2</li> <li>5120 (H) × 5120 (V)</li> <li>Test Image</li> <li>○ None</li> <li>○ Test #1</li> <li>○ Test #2</li> <li>5119</li> </ul>					
Test #3     Full     Camera Link Output Data Bit Dark Image Correction					
<ul> <li>2 Tap (Base)</li> <li>4 Tap (Medium)</li> <li>8 Tap (Full)</li> <li>10 Tap (Full)</li> </ul>		Concessing Generate Data			
VC-25MC-M30	VC-25MC-M30 43.3 °C 1C V2.210.2				

Figure 10.2 File Menu

•	Load Setting:	Loads the camera setting value from the camera memory (Factory, User1,
		User2) or user computer (From File).
•	Save Setting:	Saves the camera setting values to the camera memory (User1 or User2) or
		user computer (To File).
•	Defect Pixel:	Downloads defect information to the camera (Download to Camera) or uploads
		defect information saved in the camera to user computer (Upload to PC).
•	System Upgrade:	Upgrades MCU program or FPGA logic.
•	Exit:	Exits Configurator.

•

## 10.2.2 Start-Up

You can select the camera setting values to load when the camera is turned on.

Vieworks - VC-25N	1C-M30		
File Start-Up Tool	About		
VIEW Factory Se	tting		
Mo V User 1 Sett	ting		5110
User 2 Sett	ting	117	
		_	
Binning		5120 (H)	× 5120 (V)
Test #2 511	9		
<ul> <li>Test #3</li> </ul>	→ Full		
Camera Link Output	Data Bit		Dark Image Correction
🔘 2 Tap (Base)	🔘 8 Bit	) 10 Bit	
4 Tap (Medium)	Image Pro	cessing	Generate Data
<ul> <li>8 Tap (Full)</li> <li>10 Tap (Full)</li> </ul>	Defect	Corr.	Save Data
VC-25MC-M30			43.3 ℃ 🚺 V2.2.10.2

Figure 10.3 Start-Up Menu

- Factory Setting: Loads the camera setting values from Factory Space.
  - **User1 Setting:** Loads the camera setting values from User1 Space.
- User2 Setting: Loads the camera setting values from User2 Space.

### 10.2.3 Tool

Vieworks - VC-2	5MC-M30	
File Start-Up To	ool About	
VIEW MODE/E	Refresh	
Mode Nomal ROI	Terminal Color Calibration Factory Setting	5119
Binning	High Speed	
<pre>     1x1     2x2 </pre>	5120 (	H) × 5120 (V)
Test Image		
None		
C Test #1	110	
○ Test #2	5115	
Test #3	Full	
Camera Link Output	It Data Bit	Dark Image Correction
🔘 2 Tap (Base)	🖲 8 Bit 🔘 10 Bit	Conorato Data
4 Tap (Medium)	) Image Processing	Generate Data
Ill Tap (Full)	Defect Corr.	Save Data
VC-25MC-M30		43.3 ℃ 🚺 V2.2.10.2

Figure 10.4 Tool Menu

• **Refresh:** Loads and displays the current camera setting values on Configurator.

**Terminal:** Displays user commands in Terminal window under GUI.

To hide Terminal window, uncheck **Terminal** by clicking again.

Color Calibration: Displays the Color Calibration window for Bayer sensor color temperature calibration. When you click the Auto White Balance button, white balance is adjusted once and then Off.

Color Calibrai	ton			
- Bayer Gain				
	Red Gain	Green	Blue Gain	
	1.00000	1.00000	1.00000	
_				
	Color Reset	Auto	White Balance	

Figure 10.5 Color Calibration (VC-12MC Only)

- Factory Setting: Not supported in the user side.
- **High Speed:** Not supported on VC Series.

## 10.2.4 About

😨 Vieworks - V	/C-25N	IC-M3	0				
File Start-Up	Tool	Ab	out				
VIEW MODE/	EXP A	N	Camera Info				
Mode		_	0 + F	↓ 5119			
Nomal ROI							
Binning		0					
<ul> <li>● 1x1</li> <li>○ 2x2</li> </ul>			5120 (H)	× 5120 (V)			
Test Image							
None	None						
○ Test #1							
Test #2	Test #2 5119						
Test #3	-	Full					
- Camera Link C	Dutput	Dat	a Bit	Dark Image Correction			
🔘 2 Tap (Ba	🔘 2 Tap (Base)		8 Bit 🔘 10 Bit	Generate Data			
4 Tap (Medium)		Ima	ge Processing				
<ul> <li>8 Tap (Full)</li> <li>10 Tap (Full)</li> </ul>			Defect Corr. Save Data				
VC-25MC-M30				43.3 ℃ 🚺 V2.2.10.2			

About						
vieworks INDUSTRIAL CAMERA						
Camera Information						
Camera Name : VC-25MC-M30						
Serial Number : Z9-MCEABU012						
FPGA Version: 00.91						
MCU Version: 00.94						
Copyright (c) Vieworks. 2011 All rights reserved.						
Configurator Version : V2.2.10.2						

Figure 10.6 About Menu

Camera Info:

Displays camera information (product name, serial number, version, etc).

## 10.3 Tab

## 10.3.1 VIEW tab

VIEW tab allows you to set the camera readout mode, test image mode, data bit, channel, LUT, image processing, etc.

Vieworks - VC-25MC-M30							
<u>F</u> ile <u>S</u> tart-Up <u>T</u> ool	<u>A</u> bout						
VIEW MODE/EXP A	NALOG						
Mode Nomal	0	↓ 5119					
© ROI	ס						
Binning	ī						
	5120	$(H) \times 5120 (V)$					
© 2x2	5120	(II) × 3120 (V)					
Test Image	_						
None							
© lest #1 5119							
lest #2							
Iest #3	Full						
Camera Link Output	Data Bit	Dark Image Correction					
🔘 2 Tap (Base)	🖲 8 Bit 🔘 10 Bi	t Construction Dectar					
O 4 Tap (Medium)	Image Processing	Generate Data					
8 Tap (Full)	Defect Corr	Save Data					
U To Tap (Full)	Delect Con.						
VC-25MC-M30		43.3 ℃ 🚺 V2.2.10.2					

Figure 10.7 VIEW Tab

• Mode:

Selects readout mode. If ROI is selected, ROI setting area is activated and ROI can be set by entering desired values. If Binning is selected,  $\times 2$  option button is activated (Supported only on VC-25MC).

Selects whether to apply test image and the type of test image.

- Test Image:
- Camera Link Output:
- Data Bit:
- Image Processing:
- Dark Image Correction:

Sets Defect Correction feature On or Off. Corrects Fixed Pattern Noise at camera dark image. (Not supported on VC-25MC)

Selects Camera Link output mode.

Selects width of data output.

### VIEWORKS

Vieworks - VC-12MC-M65								
File Start-Up Tool About								
VIEW MODE/EXP ANALOG FFC								
Mode								
Full								
ROI								
Binning								
🔘 1x1	<ul> <li>1x1</li> <li>2x2</li> <li>4096 (H) × 3072 (V)</li> </ul>							
2x2								
Test Image								
None								
Test #1								
Test #2								
C Test #3								
Camera Link Out	put - Data Bit	Dark Image Correction						
2 Tap (Base)	) 💿 8 Bit 💿 10 Bit							
O 4 Tap (Mediu	im) Image Processing	Generate Data						
8 Tap (Full)	Flat Field Corr.	Save Data						
Defect Corr.								
>> set 10 OK (10	09 ms)	26.9 °C 🚺 V2.2.12						

Figure 10.8 VIEW tab for VC-12MC

- Full: Disables the Multi-ROI feature and set to its full resolution  $(4096 \times 3072)$ .
- ROI:

Displays the Setting Multi-ROI window for setting Multi-ROI.

Num	Offset X	Offset Y	Width	Height	Status
0	0	0	4096	3072	ON
1	0	0	4096	3072	OFF
2	0	0	4096	3072	OFF
3	0	0	4096	3072	OFF
4	0	0	4096	3072	OFF
5	0	0	4096	3072	OFF
6	0	0	4096	3072	OFF
7	0	0	4096	3072	OFF
8	0	0	4096	3072	OFF
9	0	0	4096	3072	OFF
10	0	0	4096	3072	OFF



• Flat Field Correction:

Sets the Flat Field Correction feature On or Off.

## 10.3.2 MODE/EXP Tap

MODE/EXP tab allows you to select trigger mode, exposure time and strobe.

💿 Vieworks - VC-25MC-M30									
<u>File</u> <u>Start-Up</u> <u>T</u> ool <u>A</u> bout									
VIEW MODE/EXP ANALOG									
Trigger Mode									
Free-Run									
() E)	kternal Sync								
[	Exposure Source Polarity								
	O Pulse	Width	@ C	C1 port	O Ac	Active Low			
	Program			External Port		Active High			
Expo	sure Time								
-						10			
- 6				н н.					
10 us	s 1 n	ns	10	0 ms	7 s	10 us			
Strobe	Strobe Type Strobe Polarity								
O N	O Narrow Output O Active Low								
O Wide Output				Active High					
VC-25IV	IC-M30				43.3 ℃	V2.2.10.2			

Figure 10.10 MODE/EXP Tab

- Trigger Mode: Selects trigger mode. Once a mode has been selected, related selections will be activated.
- **Exposure:** Selects exposure source.
- Source: Selects trigger source.
- Polarity: Selects polarity of trigger input.
- **Exposure Time:** Sets exposure time when trigger mode is set with Free-Run mode or when
  - Exposure is set with program.
- Strobe Type: Sets strobe type.
- **Strobe Polarity:** Sets the polarity of strobe output signal.

## 10.3.3 ANALOG Tab

ANALOG tab allows you to set gain and offset settings of the image.

Vieworks - VC-	25MC-M30			
File Start-Up	<u>F</u> ool <u>A</u> bout			
VIEW MODE/EXP	ANALOG			
Video Gain				
-0	1	1 1	1	2
0	:	31	63	
0		6	12	[dB]
Video Offset				
Ū	1	1 I	1	0
0	:	31	63	
VC-25MC-M30			43.3 °C	V2.2.10.2

Figure 10.11 ANALOG Tab

- Video Gain: Sets gain value.
- Video Offset: Sets offset value.

### 10.3.4 FFC Tab

Configurator provides the **FFC** tab for the VC-12MC model as follows.

💿 Viewor	rks - VC-1	2MC-C65				- • <b>- ×</b>			
<u>F</u> ile <u>S</u> ta	rt-Up <u>T</u>	ool <u>A</u> bou	ut						
VIEW N	IODE/EXP	ANALOG	FFC						
- FFC Dat	a				Flash Mem	ory			
Nur	nber of ac	quisition fra	me		Load f	rom Flash			
1	2	4 8	16		Save	to Flash			
	Generate								
- FFC Dat	FFC Data Download / Upload								
	Do	wnload to c	amera		Upload to	o PC			
FFC Off	set Level								
Ū,	1	1		1	1	-1 ×			
0		204	7		4095				
VC-12MC-	-C65				50.1 ℃	V2.2.10.2			

Figure 10.12 FFC Tab (VC-12MC Only)

 FFC Data: Generates the Flat Field Correction data to be used for correction. You can set only one frame for correction.
 Flash Memory: Saves (Save to Flash) the generated FFC data to Flash in order to reuse in the future or loads (Load From Flash) the saved FFC data.
 FFC Data Download / Upload: Downloads (Download to camera) FFC data from the user computer or uploads (Upload to PC) FFC data to the user computer.
 FFC Offset Level: Sets the offset value of the image after applying the Flat Field Correction.

# **11 Troubleshooting**

- If no image is displayed on the screen,
  - Ensure that all the cable connections are secure.
  - Ensure that the power supply is properly connected.
  - Ensure that trigger signal is applied correctly when you use external trigger mode.
- If images are not clear,
  - Ensure the camera lens or glass is clean.
  - Check that the lens aperture is adjusted properly.
- If images are dark,
  - Ensure that your camera lens is not blocked.
  - Check that exposure time is set properly.
- If you identify abnormal operation or overheating sign,
  - Ensure that the power supply is properly connected.
  - <sup>a</sup> Stop using the camera when you notice smoke or abnormal overheating.
- If the trigger mode is not working correctly,
  - Ensure that CC1 setting on the grabber board is configured correctly when you use CC1 trigger mode.
  - Ensure that cable connections are secure when you use external trigger mode.
- If you identify the difference between left and right image,
  - Check whether left and right gain settings are different.
  - Check whether left and right offset settings are different.
- If there is a communication failure between the camera and PC,
  - Ensure that the camera link cable is connected properly.
  - Ensure that serial communication settings are configured properly on both PC and frame grabber.
# Appendix A Defective Pixel Map Download

- 1. Create the Defective Pixel Map data in Microsoft Excel format as shown in the left picture below and save as a CSV file (\*.csv). The picture in the right shows the created Excel file opened in Notepad. The following rules need to be applied when creating the file.
  - Lines beginning with ':' or '--' are treated as notes.
  - Each row is produced in the order of the horizontal and vertical coordinate values.
  - The input sequence of pixel is irrelevant.





2. Select File > Defect Pixel > Download to Camera on Configurator.

😨 Vieworks - VC-25N	1C-M30		
File Start-Up Tool	About		
Load Setting	•	1	
Save Setting	+	0 + >	4 > 5119
Defect Pixel	•	Down	load to Camera
System Upgrade	• •	Uploa	d to PC
Exit	1		
1x1     2x2		5120 (H)	× 5120 (V)
Test Image			
🔘 None 🔽	ī		
Test #1			
Test #2 511	9		
Test #3	Full		
Camera Link Output	- Data Bit		Dark Image Correction
🔘 2 Tap (Base)	8 Bit	🔘 10 Bit	
🔘 4 Tap (Medium) - Imag		ocessing	Generate Data
8 Tap (Full)			Save Data
(Full)	Defec	t Corr.	
VC-25MC-M30			43.3 ℃ 🚺 V2.2.10.2

3. Search and select the created file and click **Open**.

💿 Open				<b>x</b>
Look <u>i</u> n:	🔒 Upgrade	•	G 🏂 📂 🛄 🗸	
e	Name	*	Date modified	Туре
Recent Places	defect		7/30/2013 10:21 AM	Microsoft
Desktop				
Libraries				
Computer				
	•	m		F
Network	File <u>n</u> ame:	defect	•	Open
	Files of type:	CSV files (*.csv)	<b>•</b>	Cancel

4. Configurator starts downloading defective pixel map data to the camera and downloading status is displayed at the bottom of the window.

😨 Vieworks - VO	💿 Vieworks - VC-25MC-M30 📃 🖃 💌					
<u>F</u> ile <u>S</u> tart-Up	<u>T</u> ool <u>A</u> b	out				
VIEW MODE/EX	KP ANALO	G				
Mode		0 + >	4 > 5119			
Nomal						
C ROI						
	0					
Binning						
Ix1	w.	E120 (U)	× E120 (V)			
© 2x2		5120 (H)	x 5120 (V)			
Test Image						
None	*					
Test #1	<b>5</b> 110					
Test #2	5115					
Test #3	Full	]				
Camera Link Ou	tput Dat	a Bit	Dark Image Correction			
🔘 2 Tap (Base	e) 💿	8 Bit 🔘 10 Bit				
O 4 Tap (Medi	um) Ima	ge Processing	Generate Data			
8 Tap (Full)		Defect Corr	Save Data			
U TO Tap (Full	,	Delect Corr.				
and the second se	ed!	50 %	Cancel V2.2.10.2			

- 5. Once the download has been completed, the saving process will begin. During the saving process, make sure not to disconnect the power cord.
- 6. Once all the processes have been completed, **Download completed** message will appear at the bottom of the window.

💿 Vieworks - VC-25M	1C-M30		
<u>File</u> <u>Start-Up</u> <u>T</u> ool	<u>A</u> bout		
VIEW MODE/EXP A	NALOG		
Mode		0 4 1	5119
Nomal			
C ROI			
Binning	0		
1x1     1			
© 2x2		5120 (H)	× 5120 (V)
Test Image			
None	3		
Test #1			
© Test #2 511	9		
Test #3	+ Full		
Camera Link Output	- Data Bit-		Dark Image Correction
🔘 2 Tap (Base)	8 Bit (	河 10 Bit	
O 4 Tap (Medium)	Image Pro	ocessing	Generate Data
<ul> <li>8 Tap (Full)</li> <li>10 Tap (Full)</li> </ul>	Defec	t Corr.	Save Data
Download completed			43.3 ℃ 🚺 V2.2.10.2

## Appendix B Field Upgrade

## B.1 MCU

1. Select File > System Upgrade > MCU Upgrade on Configurator.

💿 Vieworks - VC-25M0	C-M30			
File Start-Up Tool	About			
Load Setting	×.	1		
Save Setting	×	0 4 >	∢ → 5119	
Defect Pixel	<u>+</u>			
System Upgrade	+	MCU	Upgrade	
Exit		FPGA	Upgrade	
● 1x1				
2x2		5120 (H)	× 5120 (V)	
Test Image				
None				
Test #1				
© Test #2 5119				
Test #3	Full			
Camera Link Output	Data Bit		Dark Image Correction	
2 Tap (Base)	8 Bit	🔘 10 Bit		
O 4 Tap (Medium) Ima		rocessing	Generate Data	
8 Tap (Full)			Save Data	
10 Tap (Full)     Defect Corr.				
Download completed			43.3 ℃ 🚺 V2.2.10.2	

2. Search and select the provided MCU upgrade file (\*.srec) then click **Open**.

😨 Open		×
Look <u>i</u> n:	🐌 Upgrade 👻	G 🤌 📂 🛄 -
Recent Places Desktop Libraries Computer	Name	Date modified Type 1/31/2013 3:36 PM SREC File
Network	Image:     Image:       File name:     VC2_25M_00_94       Files of type:     File(".srec)       Open as read-only	<ul> <li>✓ Open</li> <li>✓ Cancel</li> </ul>

3. Configurator starts downloading MCU upgrade file to the camera and downloading status is displayed at the bottom of the window. If you want to cancel the upgrade process, click **Cancel**. This process requires several minutes to complete.

💿 Vieworks - VC-:	25MC-M3	0	
<u>File</u> <u>Start-Up</u>	ool <u>A</u> b	out	
VIEW MODE/EXP	ANALO	G	
Mode Nomal ROI		0 4 >	<b>4 ≻</b> 5119
Binning Ix1 2x2	0	5120 (H)	× 5120 (V)
Test Image None Test #1 Test #2	<u>↑</u> 5119		
Test #3	Full		
Camera Link Outp 2 Tap (Base) 4 Tap (Mediur 8 Tap (Full) 10 Tap (Full)	n) Ima	a Bit 8 Bit () 10 Bit ge Processing Defect Corr.	Dark Image Correction Generate Data Save Data
mple		38 %	Cancel V2.2.10.2

4. Once the download has been completed, the saving process will begin. During the saving process, the camera cannot be restored if a power failure occurs. Make sure that the power connection is secure.

💿 Vieworks - VC-25N	MC-M30				
<u>File</u> <u>Start-Up</u> <u>T</u> ool <u>A</u> bout					
VIEW MODE/EXP A	ANALOG				
Mode	0		4	5119	
Nomal					
C ROI					
Binning	0				
1x1     1	n ▼				
© 2x2	5	120 (H)	× 5120 (	v)	
Test Image					
None	*				
Test #1	×				
Test #2	19				
Test #3	→ Full				
Camera Link Output	Data Bit		- Dark Image	e Correction –	
🔘 2 Tap (Base)	🖲 8 Bit 🥘	) 10 Bit			
O 4 Tap (Medium)	Image Proc	cessing	Gener	ate Data	
<ul> <li>8 Tap (Full)</li> <li>10 Tap (Full)</li> </ul>	Defect	Corr.	Save	e Data	
Download completed			43.3 ℃	V2.2.10.2	

 Once all the processes have been completed, turn the power off and turn it back on again. Select Tool > Terminal and enter the 'gmv' command to confirm the version. Or, select About > Camera Info to confirm the MCU version.

About
vieworks
Camera Information
Camera Name : VC-25MC-M30
Serial Number: Z9-MCEABU012
FPGA Version: 00.91
MCU Version: 00.94
Copyright (c) Vieworks. 2011 All rights reserved. Configurator Version: V2.2.10.2

### B.2 FPGA

1. Select File > System Upgrade > FPGA Upgrade on Configurator.

😨 Vieworks - VC-25N	/IC-M30		
File Start-Up Too	I About		
Load Setting	+		
Save Setting		0 4 >	5119
Defect Pixel	• • ]		
System Upgrade	: ▶	MCU	Upgrade
Exit		FPGA	Upgrade
Ix1			
© 2x2		5120 (H)	× 5120 (V)
Test Image			
None	ন		
🔘 Test #1	2		
© Test #2 511	9		
Test #3	Full		
Camera Link Output	Data Bit		Dark Image Correction
🔘 2 Tap (Base)	8 Bit	河 10 Bit	
🔘 4 Tap (Medium)	Image Pro	ocessing	Generate Data
8 Tap (Full)	_		Save Data
10 Tap (Full)	Defec	t Corr.	
Download completed			43.3 ℃ 🚺 V2.2.10.2

2. Search and select the provided FPGA upgrade file (\*.bin) and click **Open**.

💿 Open				×
Look <u>i</u> n:	🍌 Upgrade	•	G 🤌 📂 🛄 -	
Recent Places	Name	^ 00_91.bin	Date modified 11/1/2012 4:48 PM	Type BIN File
Computer Computer Network	<ul> <li>✓ Interpretended</li> <li>File name:</li> <li>Files of type:</li> </ul>	TT VC-25M30_V00_91 Binary File(*.bin)		Open Cancel

3. The subsequent processes are identical to those of MCU upgrade.







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